

2811/1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of:

Alan R. Reinberg et al.

Serial No.: 09/740,256

Filed: December 19, 2000

For: METHOD OF FABRICATING
A CONDUCTIVE PATH
IN A SEMICONDUCTOR DEVICE

§
§ Group Art Unit: 2811
§

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§ Examiner: Tran, T.
§

§
§ Atty. Docket: MCRO:106-2
§ 95-0412.02
§

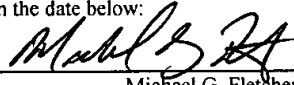
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37 C.F.R. 1.8

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November 3, 2003

Date


Michael G. Fletcher

Sir:

RESPONSE AND AMENDMENT

In response to the Official Action mailed on June 3, 2003, please amend the above-identified application as follows:

IN THE TITLE

Please delete "Method of Fabricating A Conductive Path in a Semiconductor Device" and insert -An X-point Memory Cell --, therefor.

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